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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/624,716
Filing Date July 21, 2003
Inventor..... Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner S. Geyer
Attorney's Docket No. MI22-2357
Title: Methods of Forming Semiconductor Constructions

INFORMATION DISCLOSURE STATEMENT**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. No admission is made regarding whether the listed references are prior art.

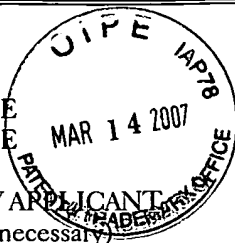
Citation of these references is respectfully requested.

Respectfully submitted,

Dated: March 14, 2007

By: Jennifer J. Taylor
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Reg. No. 48,711

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
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(Use several sheets if necessary)ATTY. DOCKET NO.
MI22-2357SERIAL NO.
10/624,716APPLICANT
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7/21/03 [RCE filed herewith]GROUP
2812

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,121,666	9/19/00	Burr			
	AB	2004/0070030 A1	4/15/04	Chindalore et al.,			
	AC	2006/0202287 A1	9/14/06	Noda et al.			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-class	Translation	
							Yes	No
	AK							
	AL							
	AM							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AN	
	AO	
EXAMINER	DATE CONSIDERED	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.